



PTO/SB/08A (10-01)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Application Number	10/084,873
Filing Date	03/01/2002
First Named Inventor	Yue Xiao et al.
Group Art Unit	2812
Examiner Name	
Attorney Docket Number	1987.EEM

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item, (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MZ	.	YALA, NADIA et al.: "Characterization of Flip Chip Assembly Utilizing Wafer Applied Underfill"; 2002 Electronic Components and Technology Conference	

Examiner Signature		Date Considered	8/22/3
#49333			

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/06A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031
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Sheet 1 of 1			EXAMINER NAME: First Named Inventor: YUE XIAO et al.				
			Filing Date: 03/01/2002			Group Art: 2812	
U.S. PATENT DOCUMENTS							
*Examiner Initials	Cite No. ¹	Document Number Number - Kind Code ² (If known)	Pub. Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document			Pages
(Q)		US- 5,855,821	1/5/99	M. Chau et al.			
(Q)		US- 6,132,646	10/17/00	Z. Zhou et al.			
(Q)		US- 6,121,689	9/19/00	M.A. Capote et al.			
(Q)		US- 6,180,696 B1	1/30/01	C-P Wong et al.			
(Q)		US- 6,194,788 B1	2/27/01	K. B. Gilleo et al.			
		US-					
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FOREIGN PATENT DOCUMENTS							
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(Q)		JP 10-101906A (45-753)	4/21/98	Shinetsu Kagaku			
(Q)		JP 62-081416A (45-754)	4/14/87	Denki Kagaku			
(Q)		WO 97/07541	27/2/97	K.J. Kirsten et al.			
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